

Technical Information

Experion Series-C I/O Specification



**EP03-490-520**

**Release 520**

**Dec 2021, Version 1.5**

## 5.20. Universal Input Output – CC-PUIO31

### Function

The Universal Input Output module interfaces with analog input, analog output, digital input, and digital output field devices.

### Notable Features

- Each channel user configurable as:
  - Analog Input
  - Analog Output
  - Digital Input
    - DISOE - Sequence of Events (1ms resolution SOE, 20ms PV scan)
  - Digital Output
  - Pulse Input (any four channels)
- Open Wire Detection
- Electronic short circuit protection<sup>1</sup>
- Fast Scan (Priority I/O Module Scan)
- Safe-state (FAILOPT) behaviors configurable on a per channel basis for Digital / Analog Output
- HART 7 support (Analog I/O)
- HART Modem per Channel for Fast Performance
- Extended Temperature Range -40 to +70°C module ambient

### Model Specifications

Parameter	Specification		
Universal Process IO Module	<b>CC-PUIO31</b>		
IOTA Model Numbers	<b>CC-TUIO31</b>	Non Redundant	9"
	<b>CC-TUIO41</b>	Redundant	12"
Note 1: Each signal can be shorted in the field with no damage to the IOM or IOTA. Other channels on the same IOM will not be affected. AI, AO and DI channels are further protected and certified to support energy limited / nonincendive field wiring connections to Zone 2 / Division 2 hazardous locations.			

**Detail Specifications - Analog Input**

Parameter	Specification
Input type	Current (2, 3, or 4 wire devices)
Input Channels	32 Maximum per module (with or without open wire detect)
A/D Converter Resolution	16 bit
Input Range	0-20 mA or 4-20 mA
Normal Mode Rejection Ratio	18 dB at 50 Hz, 20 dB at 60 Hz
Input Filter Response	Single pole, -3dB @ 6 Hz
Crosstalk, dc to 60 Hz (channel-to-channel)	60 dB
Input Impedance	250 $\Omega$
Input Voltage Range (any input referenced to common, no damage)	+33 VDC to -1 VDC <sup>(1)</sup>
Input Scan Rate	10 ms
Hardware accuracy	0.1% of full-scale (23.5 $\pm$ 2°C) 0.175% of full-scale (0 to +70°C) 0.25% of full-scale (-40 to +70°C)
Short Circuit Current Limit	25 mA
Note 1: Return terminal is connected directly to Common (Ground)	

**Detail Specifications - Pulse Input**

Parameter	Specification
Channels	Any 4 channels
Frequency	0-15 kHz
Minimum Pulse Width	25 $\mu$ s
Duty Cycle	Any Duty Cycle that meets the Minimum Pulse Width specification

**Detail Specifications - Analog Output with HART<sup>1</sup>**

Parameter	Specification
Output Type	4-20 mA
Output Channels	32 Maximum per module <sup>2</sup>
Output Temperature Drift	0.007% of Full Scale/°C
Output Readback Diagnostic	± 4% of Full Scale
Output Current Linearity	± 0.05% of Full Scale (nominal)
Resolution	12 bit
Calibrated Accuracy	< 0.35% of Full Scale (25°C) including linearity
Directly Settable Output Current Range	0 mA, 2.9 mA to 21.1 mA
Maximum Resistive Load (24 V supply)	825 ohms @ 20 mA
Maximum Output Compliant Voltage (24 V supply)	16.5 VDC @ 20 mA
Open Circuit Voltage	Supply Voltage (26 VDC maximum)
Response Time, DAC input code to output (within 3% of final value)	80 ms in HART mode 0.25 ms in non-HART mode
Gap (0 mA) of Output to Field on Switchover	0 ms (both partners continuously active)
<p>Note 1: If an AO channel's configuration is changed from non-HART to HART, and the analog OP remains energized, there will be a disturbance of the output of no more than 65ms while the channel is reloaded. Changing an AO channel from HART to non-HART will not produce any OP disturbance.</p> <p>Note 2: Please refer to the User's Guide for calculation method to determine channel usage versus operating temperature.</p>	

**Detail Specifications - Digital Input with OWD<sup>1</sup>**

Parameter	Specification
Open Circuit Voltage	Supply Voltage (26 VDC maximum)
Closed contact current	7 mA (maximum)
Closed contact detection	$3.1 \text{ mA} < I < 7 \text{ mA}$
Open contact detection	$0.9 \text{ mA} < I < 2.0 \text{ mA}$
Open wire detect	$I < 0.9 \text{ mA}$
Digital Input Resolution for Sequence of Events (SOE)	1ms (any /all Channels)
Note 1 Please refer to the User's Guide for appropriate field resistor configuration for OWD function.	

**Detail Specifications - Digital Input without OWD**

Parameter	Specification
Open Circuit Voltage	Supply Voltage (26 VDC maximum)
Closed contact current	7 mA (maximum)
Closed contact detection	$I > 3.2 \text{ mA}$
Open contact detection	$I < 2.0 \text{ mA}$
Digital Input Resolution for Sequence of Events (SOE)	1ms (any /all Channels)

**Detail Specifications - Digital Output**

Parameter	Specification
Output Channels	32 Maximum per module <sup>2</sup> (with or without open wire detect)
Output Type	Solid state source, short circuit protected <sup>3</sup>
Load Current Off	< 0.1 mA
Load Current On	1 mA Minimum to 0.5 A Maximum per channel 9 A Maximum per module <sup>1</sup>
On-State Voltage	24 V (typ), load current @ 0.5 A
Off-State Voltage	0 V
Off-State Leak Current	< 0.1 mA
Gap (0 current) of Output to Field on Switchover	0 ms
<p>Note 1: Dependent on actual channel configuration for the module and the environment. Please refer to the User's Guide for calculation method.</p> <p>Note 2: Please refer to the User's Guide for calculation method to determine channel usage versus operating temperature.</p> <p>Note 3: Short circuit will be limited to less than 1.4 Amps within 2 microseconds, and to 750 mA within 10 microseconds. If the short period persists more than 10 mili seconds, the DO channel will be shutoff</p>	