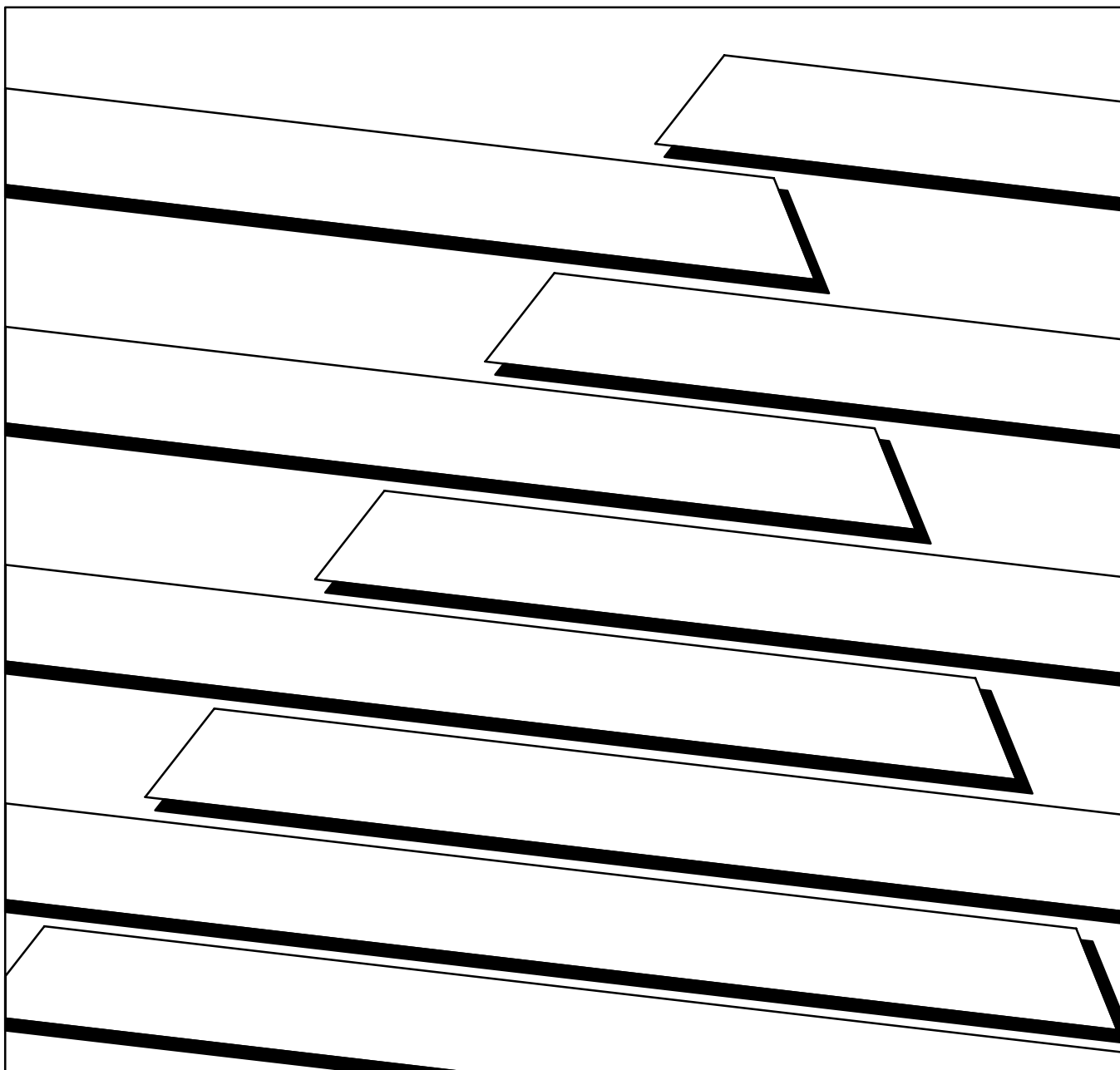




ALLEN-BRADLEY

1791 Discrete I/O AC and DC Block I/O Input and Output Modules

User Manual



Connections	1791-32A0 Series B		Connector/Terminal	
	Designation	Description	Left Conn.	Right Conn.
Power Connections	L1	ac hot	1	
	N	ac neutral	3	
	GND	Chassis ground	2 ¹	
Remote I/O Connections	BLU	Blue wire – RIO	6	
	CLR	Clear wire – RIO	8	
	SHD	Shield – RIO	7	
I/O Connections				
Input (G) ⁶	in 00 thru in 07	Input 00 thru 07	16, 18, 20, 22, 24, 26, 28, 30	
	COM 1	L2/N Input Common	10, 12, 14 ²	
	COM 2	L2/N Input Common	9, 11, 13 ³	
	PLC: in 10 thru in 17 SLC: in 08 thru in 15	PLC: Input 10 thru 17 SLC: Input 08 thru 15	15, 17, 19, 21, 23, 25, 27, 29	
Input (G+1) ⁷	in 00 thru in 07	Input 00 thru 07		15, 13, 11, 9, 7, 5, 3, 1
	COM 3	L2/N Input Common		17, 19, 21 ⁴
	COM 4	L2/N Input Common		18, 20, 22 ⁵
	PLC: in 10 thru in 17 SLC: in 08 thru in 15	PLC: Input 10 thru 17 SLC: Input 08 thru 15		16, 14, 12, 10, 8, 6, 4, 2
	Not used	For internal test only; not for customer use.	4, 5	29, 27, 26
	No Conn	No internal connection; customer can use.		30, 28, 25, 24, 23

¹ Connect chassis ground to equipment grounding stud. These are not internally connected.

² Left connector terminals 10, 12 and 14 are internally connected together.

³ Left connector terminals 9, 11 and 13 are internally connected together.

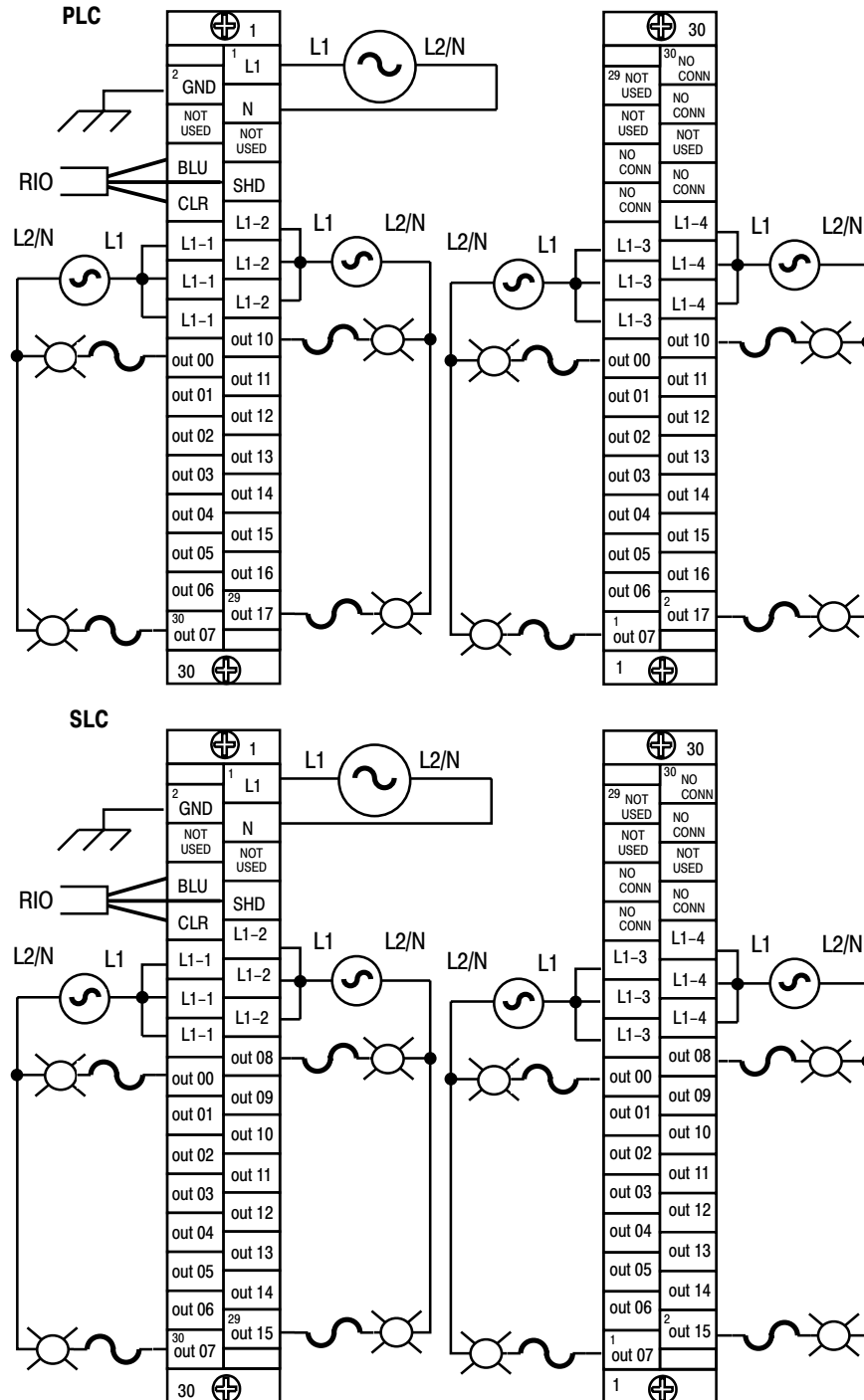
⁴ Right connector terminals 21, 19 and 17 are internally connected together.

⁵ Right connector terminals 22, 20 and 18 are internally connected together.

⁶ IN (G) = input module group (1 word).

⁷ IN (G+1) = input module group plus 1 word.

Figure 3.10
Output Wiring Connections for the 1791-0A32 Series B



NOTE: L1-1 connections must be externally connected together to accommodate total amperage.
 L1-2 connections must be externally connected together to accommodate total amperage.
 L1-3 connections must be externally connected together to accommodate total amperage.
 L1-4 connections must be externally connected together to accommodate total amperage.
 Output fusing is recommended. Refer to Table 2.B on page 2-8.

surge suppression, [3-34](#)
switch settings, [4-2](#)
 complementary I/O, [4-10](#)
 complementary rack, [4-4](#)
 primary rack, [4-3](#)

T

terminal block
 pin numbering, [3-5](#)
 removal, [3-4](#)
termination resistor, [3-34](#)
throughput requirements, [3-38](#)
troubleshooting chart, [5-2](#)
Types of block I/O, [1-1](#)
types of block I/O, [P-2](#)

W

wiring connections, [3-4](#)
 1791-0A16, [3-7](#)

1791-0A32, [3-12](#)
1791-0B16, [3-21](#)
1791-0B32, [3-26](#)
1791-16A0, [3-6](#)
1791-16AC, [3-14](#)
1791-16B0, [3-20](#)
1791-16BC, [3-28](#)
1791-24A8, [3-16](#)
1791-24AR, [3-18](#)
1791-24B8, [3-30](#)
1791-24BR, [3-32](#)
1791-32A0, [3-10](#)
1791-32B0, [3-24](#)
1791-8AC, [3-8](#)
1791-8AR, [3-9](#)
1791-8BC, [3-22](#)
1791-8BR, [3-23](#)