

PACSystems™ RX3i

IC695CMX128-AB

Control Memory Xchange Module

GFK-2506A
January 2010

The Control Memory Xchange (CMX) module provides deterministic sharing of data among PLCs and other computing devices on a high-speed fiber optic network, using reflective memory technology. A reflective memory network can contain up to 256 nodes. Each node in the network can be any reflective memory device that is compatible with the 5565 family. When data is written to one node, all nodes on the network are automatically updated with the new data.

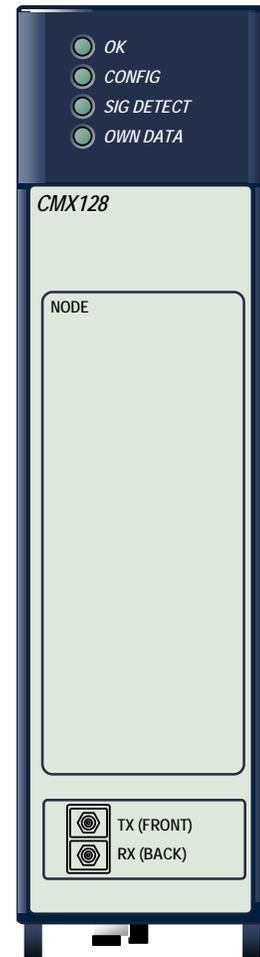
Each node in the reflective memory network is connected in a daisy-chained loop using fiber optic cables. The transmitter of the first node is tied to the receiver of the second. The transmitter of the second node is tied to the receiver of the third node, and so on, until the loop is completed back at the receiver of the first node. The figure on page 5 shows an example of a reflective memory network.

A PACSystems RX3i main rack supports a maximum of six CMX modules.

Features

- PACSystems RX3i single slot form factor.
- 128 Mbytes reflective memory with parity.
- Software configuration of all node parameters (no jumper or switch settings required).
- No RX3i CPU processing required to operate the network.
- Network-compatible with VMIC 5565 family of reflective memory devices, including the ACC-5595 reflective memory hub and the RX7i CMX module.
- Connection with multimode fiber up to 300m/984.25ft.
- Dynamic packet sizes of 4 to 64 bytes, controlled by the CMX module.
- Network transfer rate of 43 Mbyte/s (4 byte packets) to 174 Mbyte/s (64 byte packets)
- Network link speed of 2.1 Gigabits/sec.
- Programmable module interrupt output.
- Four general-purpose network interrupts with 32 bits of data each.
- Network error detection.
- Up to 256 nodes per network.
- Redundant transfer mode operation. This optional mode reduces the chance of a data packet being dropped from the network.
- Configurable network memory offset allows you to assign nodes on a network to groups according to the 16MB segment in the network address space that they use.

The CMX128 module must be located in an RX3i Universal Backplane. The module can be hot-inserted and removed following the instructions in the *PACSystems RX3i System Manual*, GFK-2314.



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Specifications

Packet size	Dynamic packet sizes of 4 to 64 bytes, automatically controlled by the CMX module
Transfer rate	Network link speed of 2.1 Gbits/sec
User memory	128MB SDRAM
Input power (from RX3i power supply)	660 mA @ +3.3 VDC 253 mA @ +5 VDC
Connectors	<ul style="list-style-type: none"> ■ Fiber optic LC type, conforms to IEC 61754-20 ■ Zirconium ceramic ferrule ■ Insertion loss: 0.35 dB (maximum) ■ Return loss: -30dB

Refer to the *PACSystems RX3i System Manual*, GFK-2314, for product standards and general specifications.

Installation in Hazardous Locations

- EQUIPMENT LABELED WITH REFERENCE TO CLASS I, GROUPS A, B, C & D, DIV. 2 HAZARDOUS LOCATIONS IS SUITABLE FOR USE IN CLASS I, DIVISION 2, GROUPS A, B, C, D OR NON-HAZARDOUS LOCATIONS ONLY
- WARNING - EXPLOSION HAZARD - SUBSTITUTION OF COMPONENTS MAY IMPAIR SUITABILITY FOR CLASS I, DIVISION 2;
- WARNING - EXPLOSION HAZARD - WHEN IN HAZARDOUS LOCATIONS, TURN OFF POWER BEFORE REPLACING OR WIRING MODULES; AND
- WARNING - EXPLOSION HAZARD - DO NOT CONNECT OR DISCONNECT EQUIPMENT UNLESS POWER HAS BEEN SWITCHED OFF OR THE AREA IS KNOWN TO BE NONHAZARDOUS.

Related Publications

PACSystems Memory Xchange Modules User's Manual, GFK-2300

PACSystems RX3i System Manual, GFK-2314

PACSystems CPU Reference Manual, GFK-2222

Proficy™ Machine Edition Logic Developer-PLC Getting Started, GFK-1918

Ordering Information

Description	Catalog Number
Control Memory Xchange Module for RX3i	IC695CMX128
Fiber Optic Cables	VMICBL-000-F5-0xx, where 0xx distinguishes different lengths
Reflective Memory Hub	VMIACC-5595

Release Information

This is the initial release of the RX3i Control Memory Xchange Module.

Release History

Release	Firmware Version	Date	Upgrade Kit	Comments
IC695CMX128-AB	1.02	October 2009	82A1512-MS10-000-A1	See "Problems Resolved."
IC695CMX128-AA	1.00	September 2008	N/A	Initial Release

Functional Compatibility

The CMX128 requires the following versions for configuration and operation.

Subject	Description
Programmer Version Requirements	Proficy® Machine Edition Logic Developer 5.80 (released build 4541) or later is required to use the RX3i IC695CMX128 modules.
RX3i CPU	PACSystems RX3i CPU firmware version 5.50 or later
Rack Location	The CMX128 must be located in the main RX3i rack. IC695CMX128 modules require a PCI backplane, which is not available on IC694CHSxxx expansion bases.

Problems Resolved by Release 1.02

Subject	Description
Memory read parity check	CMX/RMX memory read parity was checked only on DWORDS aligned to QWORD boundary. An error existed in the parity check where half of the memory was not being checked for parity. (Only the lower 32-bit DWORD for each 64-bit QWORD would be parity checked, ignoring the parity check on the upper 32-bits.)
CMX128 module power up	An error existed in the CMX transceiver control that allowed network traffic to be received while the module was powering up and executing internal initialization. Occasionally, this traffic could interfere with the memory controller initialization, causing it to lock up and preventing the module from completing power-up. The failure occurred only when a Reflective Memory Hub was connected to the CMX.
Eliminated possible metastability issue in the DDR2 memory interface.	An issue existed in the CMX DDR2 memory interface that could cause the memory data path to be in a metastable state. If this occurred, invalid data could be written to or read from the DDR2 memory.

Restrictions and Open Issues in this Release

Subject	Description
RX3i CMX/RMX does not disable transmitter when the CPU goes to Stop/Halt mode.	<p>For IC695CMX128 modules and IC695RMX128 modules not used as redundancy links, the automatic transmitter disable feature currently does not work correctly when a controller goes to Stop/Halt mode.</p> <p>When the CPU goes to Stop/Halt mode or fails and the automatic transmitter disable feature is enabled, the fiber optic transmitter should be turned off, breaking the reflective memory link. When the feature is disabled the transmitter remains ON and the reflective memory link will not be broken.</p> <p>When enabled, the automatic transmitter disable feature does not work when the CPU goes into Stop/Halt mode (such as after a software watchdog trip or multi-bit ECC error detection), leaving the fiber optic transmitter ON. The fiber optic transmitter is properly disabled if the CPU fails or is lost (for instance the CPU hardware is removed, the CPU experiences a hardware watchdog event, or displays a blink code such as a page fault).</p> <p>This user-configurable feature is enabled by default. It may be disabled by clearing bit 12 with a BUS_WRITE to region 3, offset 0x440.</p>
The LCSR status bit is not turning ON after LISR turns ON when Interrupt (Sync Loss) is generated	<p>The LCSR bit is not latched to the ON state (indicating sync loss) and should not be used for sync loss detection.</p> <p>The LISR bit should be used for detecting sync loss.</p>
SVC_REQ 17 is not supported	<p>SVC_REQ 17 is not supported to mask or unmask module interrupts on RX3i CPUs. There is currently no way to identify which module interrupt should be masked on RX3i. To turn off interrupts, use the normal interrupt disabling mechanism described in the IC695CMX128 user's manual. For details, see "Dynamic Masking of Interrupts" in the <i>PACSystems Memory Xchange Modules User's Manual</i>, GFK-2300.</p>
CPU320 compatibility issue with firmware version 5.50	<p>When using CPU320 with firmware version 5.50, occasionally, a backplane reset of the CMX128 module will cause it to become non-responsive. This problem is corrected in CPU320 firmware version 5.51.</p>