Honeywell

Technical Information

Series 8 Controller and I/O Specification

S803-150-530

Release 530

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4.3.2. I/O Module Sizes

IOTA Sizing is nominal (6in = 152mm, 9in =228mm, 12in =304mm). I/O modules are associated with their respective IOTAs in the table below. The I/O Module is supported by one or more IOTAs. Below section also provides an overview of various available IO modules, IOTA, IOTA size and redundancy features.

I/O Module (Coated)	IOTA (Coated)	Description	Circuits	Size (in “)	Red.
8C-PAIH54		High-level AI HART, Differential	16		√
	8C-TAIDA1	AI IOTA		9	
	8C-TAIDB1	AI IOTA Redundant		12	√
8C-PAIHA1		High-level AI HART, Single-ended	16		√
8C-PAINA1		High-level AI w/o HART, Single-ended	16		√
	8C-TAIXA1	AI IOTA		6	
	8C-TAIXB1	AI IOTA Redundant		12	√
8C-PAIMA1		Low-level AI – RTD & TC	16		
	8C-TAIMA1	Low-level AI IOTA		9	
8C-PAOHA1		Analog Output HART	16		√
8C-PAONA1		Analog Output w/o HART	16		√
	8C-TAOXA1	AO IOTA		6	
	8C-TAOXB1	AO IOTA Redundant		12	√
8C-PDILA1		Digital Input 24V	32		√
8C-PDISA1		Digital Input Sequence of Events	32		√
8C-PDIPA1		Digital Input 24V Pulse Accumulation	32		√
	8C-TDILA1	DI 24V IOTA		9	
	8C-TDILB1	DI 24V IOTA Redundant		12	√
8C-PDODA1		DO 24V Bussed Out	32		√
	8C-TDODA1	DO 24V Bussed IOTA		9	
	8C-TDODB1	DO 24V Bussed IOTA Redundant		12	√
	8C-SDOX01	DO Relay Extension [1]		15	√

Note 1- DO Relay Extension board is used along with DO IO module with IOTA (Redundant or non-redundant). Refer Section 4.4.11 for more details.

4.4.8. Digital Input Sequence of Events

Function

The Digital Input Sequence of Events (DISOE) accepts 24VDC discrete signals as discrete inputs. The inputs can be time tagged to support 1ms resolution Sequence of Events

Notable Features

- Three modes of operation
  - Normal (20ms PV scan)
  - Sequence of Events (1ms resolution SOE, 20ms PV scan)
  - Low Latency (5ms PV scan)
- Extensive internal diagnostics for data integrity
- Optional redundancy
- Internal or external field power selection
- On board excitation power (no need for marshalling power)
- Direct / Reverse Input Indication
- Galvanic isolation

Detailed Specification – Digital Input SOE (8C-PDISA1)

Parameter	Specification		
Input / Output Module	8C-PDISA1 - Digital Input Sequence of Events, Coated		
IOTA Modules	8C-TDILA1	Non Redundant, Coated	9”
	8C-TDILB1	Redundant, Coated.	12”
Input Channels	32		
Input Channel Scanning (PV)	Normal = 20ms ; Fast = 5ms		
Digital Input Resolution for Sequence of Events (SOE)	1ms		
Voltage Rating	24 VDC		
DI Power Voltage Range	18 to 30 VDC		
Module current rating	95 mA		
Galvanic Isolation (any input terminal voltage referenced to common)	1000 VAC RMS or ±1000 VDC		
Isolation Technique	Optical (in IOM)		
ON Sense Voltage/Current	13 VDC (min) or 3 mA (min)		
OFF Sense Voltage/Current	5 VDC (max) or 1.2 mA (max)		
Input Impedance	4.2 KΩ		
Absolute Delay Across Input Filter and Isolation	5 ms ± 20%		
Field Resistance for Guaranteed ON Condition	300 Ωmax @ 15 VDC		
Field Resistance for Guaranteed OFF Condition	30 KΩmin @ 30 VDC		
Module Removal and Insertion under power	Supported		